

RJE0601JPE

Silicon P Channel MOS FET Series Power Switching REJ03G1906-0200 Rev.2.00 Jun 29, 2010

Datasheet

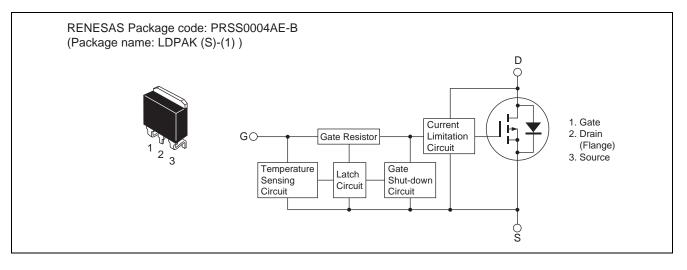
Description

This FET has the over temperature shut-down capability sensing to the junction temperature. This FET has the built-in over temperature shut-down circuit in the gate area. And this circuit operation to shut-down the gate voltage in case of high junction temperature like applying over power consumption, over current etc..

Features

- High endurance capability against to the short circuit.
- Built-in the over temperature shut-down circuit.
- Latch type shut down operation (need 0 voltage recovery).
- Built-in the current limitation circuit.
- Low on-resistance R_{DS} : 22 m Ω Typ, 27 m Ω Max ($V_{GS} = -10$ V)

Outline



Absolute Maximum Ratings

		1	$(Ta = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	-60	V
Gate to source voltage	V _{GSS}	-16	V
Gate to source voltage	V _{GSS}	2.5	V
Drain current	I _D	-40 Note 3	А
Body-drain diode reverse drain current	I _{DR}	-40	А
Avalanche current	I _{AP} ^{Note 1}	-15	А
Avalanche energy	E _{AR} ^{Note 2}	964	mJ
Channel dissipation	Pch Note 1	50	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. Value at $Tc = 25^{\circ}C$

2. Tch = 25°C, Rg \geq 50 Ω

3. It provides by the current limitation lower bound value.



2500

Typical Operation Characteristics

						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Input voltage	VIH	-3.5	_	_	V	
	VIL	_	—	-1.2	V	
Input current	I _{IH1}	_	_	-100	μΑ	$Vi = -8 V, V_{DS} = 0$
(Gate non shut down)	I _{IH2}	_	_	-50	μΑ	$Vi = -3.5 V, V_{DS} = 0$
	l _{IL}	_	_	-1	μΑ	$Vi = -1.2 V, V_{DS} = 0$
Input current	I _{IH(sd)1}	_	-0.8	_	mA	$Vi = -8 V, V_{DS} = 0$
(Gate shut down)	I _{IH(sd)2}	_	-0.35	_	mA	$Vi = -3.5 V, V_{DS} = 0$
Shut down temperature	Tsd	_	175	_	°C	Channel temperature
						(dv/dt $V_{GS} \ge 500 \text{ V/ms}$)
Gate operation voltage	Vop	-3.5	—	-12	V	
Drain current	I _{D limt}	-40	-60	—	А	$V_{GS} = -12 \text{ V}, \text{ V}_{DS} = -10 \text{ V}^{Note 4}$
(Current limitation value)						

Notes; 4. Pulse test

Electrical Characteristics

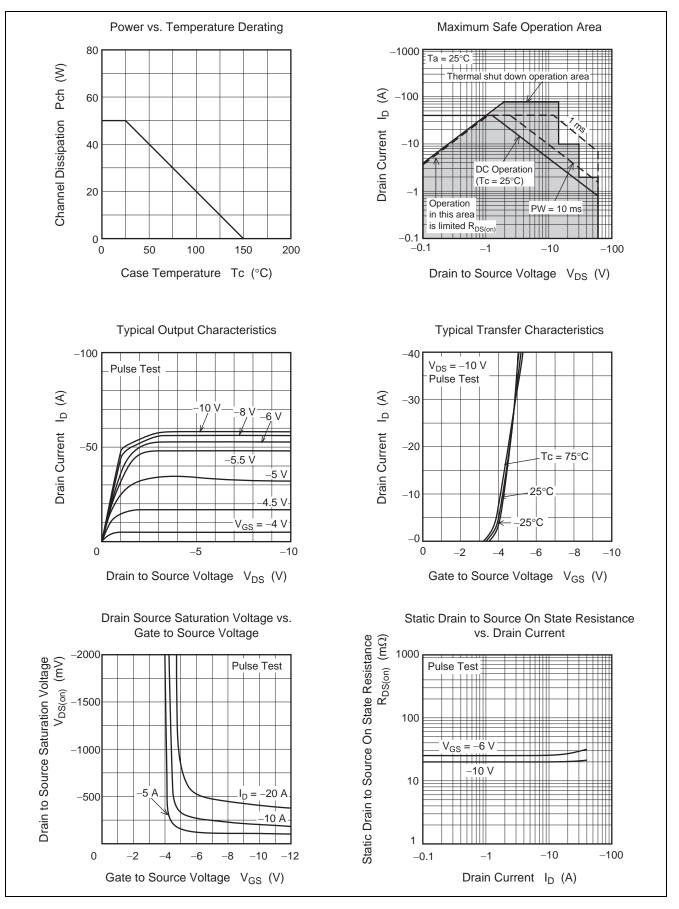
						$(Ta = 25^{\circ}C)$
ltem	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain current	I _{D1}			-40	Α	$V_{GS} = -3.5 \text{ V}, V_{DS} = -10 \text{ V}$
	I _{D2}	_	—	-10	mA	$V_{GS} = -1.2 \text{ V}, V_{DS} = -10 \text{ V}$
	I _{D3}	-40	-60	—	Α	$V_{GS} = -12 \text{ V}, V_{DS} = -10 \text{ V}^{Note 5}$
Drain to source breakdown voltage	V _{(BR)DSS}	-60	_	—	V	$I_{D} = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown	V _{(BR)GSS}	-16	—	—	V	$I_{G} = -800 \ \mu A, \ V_{DS} = 0$
voltage	V _{(BR)GSS}	2.5	—	—	V	$I_G = 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS1}	_	—	-100	μΑ	$V_{GS} = -8 V, V_{DS} = 0$
	I _{GSS2}	_	—	-50	μΑ	$V_{GS} = -3.5 \text{ V}, V_{DS} = 0$
	I _{GSS3}			-1	μΑ	$V_{GS} = -1.2 \text{ V}, V_{DS} = 0$
	I _{GSS4}			100	μΑ	$V_{GS} = 2.4 \text{ V}, V_{DS} = 0$
Input current (shut down)	I _{GS(OP)1}		-0.8		mA	$V_{GS} = -8 V, V_{DS} = 0$
	I _{GS(OP)2}		-0.35		mA	$V_{GS} = -3.5 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}			-10	μΑ	$V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	-2.2	—	-3.4	V	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1 \text{ mA}$
Forward transfer admittance	y _{fs}	8.4	27	—	S	$I_D = -20 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note 5}}$
Static drain to source on state	R _{DS(on)}	_	26	45	mΩ	$I_D = -20 \text{ A}, V_{GS} = -6 \text{ V}^{\text{Note 5}}$
resistance	R _{DS(on)}	_	22	27	mΩ	$I_D = -20 \text{ A}, V_{GS} = -10 \text{ V}^{\text{Note 5}}$
Output capacitance	Coss	—	860	—	pF	$V_{DS} = -10 V$, $V_{GS} = 0$, f = 1MHz
Turn-on delay time	t _{d(on)}	—	12.6	—	μS	$V_{GS} = -10 \text{ V}, I_D = -20 \text{ A},$
Rise time	tr	—	12.5	—	μS	$R_L = 1.5 \Omega$
Turn-off delay time	t _{d(off)}	—	3.7	—	μS	
Fall time	t _f	_	2.2	—	μS	
Body-drain diode forward voltage	V_{DF}	_	-0.95	_	V	$I_F = -40 \text{ A}, V_{GS} = 0$
Body-drain diode reverse recovery time	t _{rr}	—	100	—	ns	$I_F = -40 \text{ A}, \text{ V}_{GS} = 0$ $di_F/dt = 50 \text{ A}/\mu\text{s}$
Over load shut down operation time Note 6	t _{os1}	_	2.3	—	ms	$V_{GS} = -5 V, V_{DD} = -16 V$

Notes: 5. Pulse test

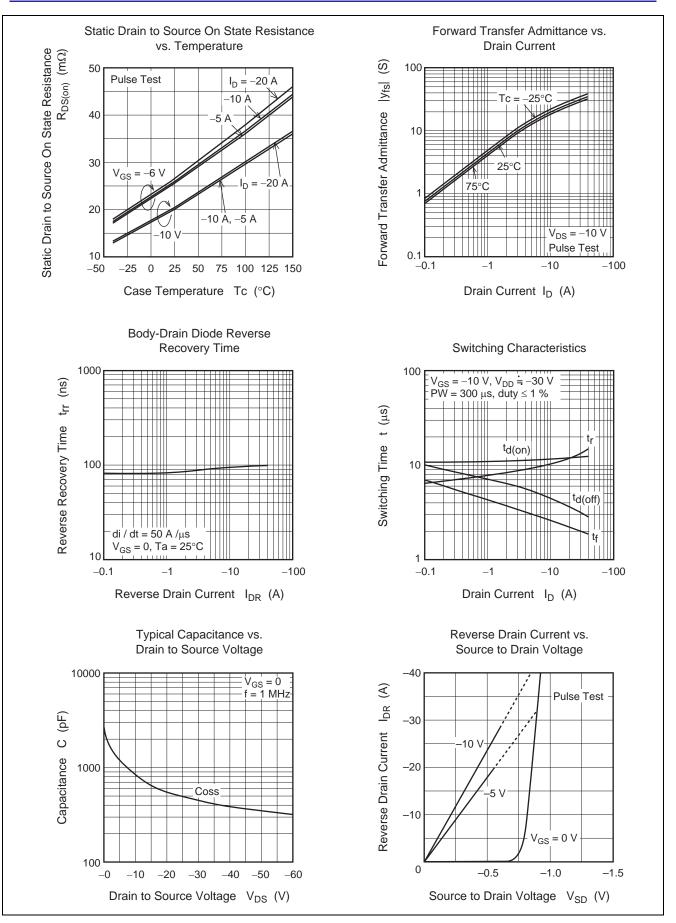
6. Including the junction temperature rise of the over loaded condition.



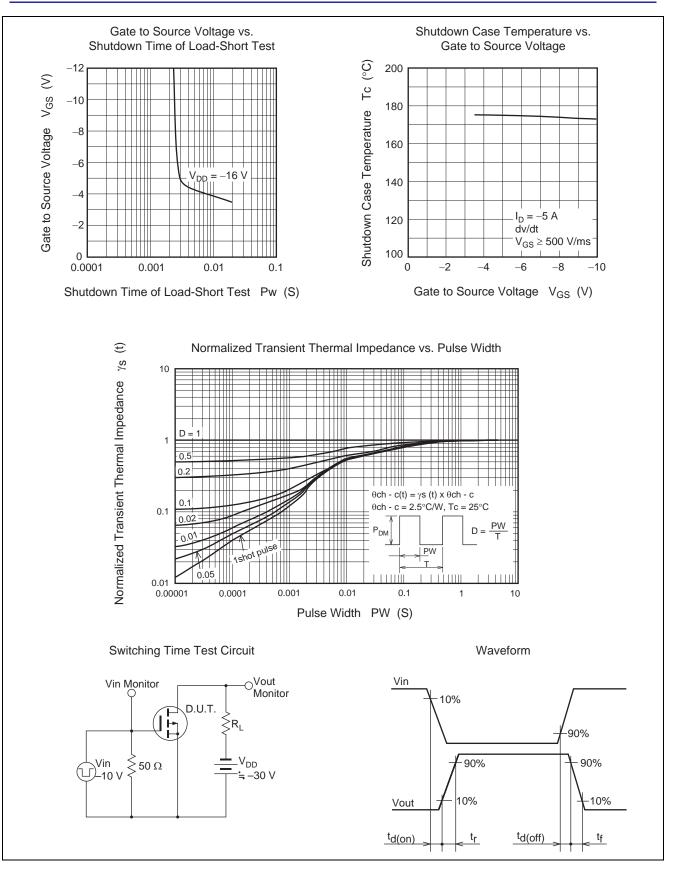
Main Characteristics



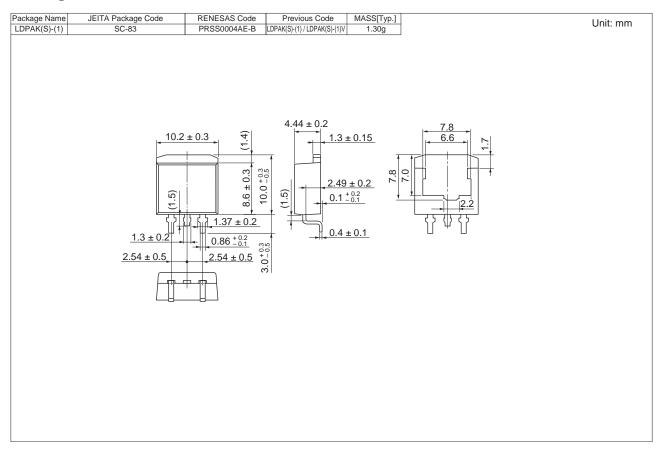








Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
RJE0601JPE-00-J3	1000 pcs	Taping



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